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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Divakaruni et al. 850

Serial No.: 09/718,180

Group Art Unit: 2811

Filed: November 22, 2000

Examiner: Gebremariam, Samuel A.

For: LOGIC STRUCTURE PROCESS AND APPLICATION FOR VERTICLE
BIPOLAR TRANSISTORCommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**SECOND AMENDMENT UNDER 37 C.F.R. §1.116**

Sir:

In response to the Office Action mailed December 30, 2003, please amend the above-identified patent application as follows:

IN THE SPECIFICATION:

Please amend page 2, lines 2- 7 of the specification as follows:

It is advantageous in semiconductor manufacturing to simultaneously produce as many different types of devices on a chip has as possible. Such simultaneous production reduces the number of steps and the amount of material required to make the chip. This reduces the time and cost of producing semiconductor chips. Therefore, it is desirable to simultaneously form different types of transistors on a single chip.